



NAND Flash Spare Area Assignment Standard

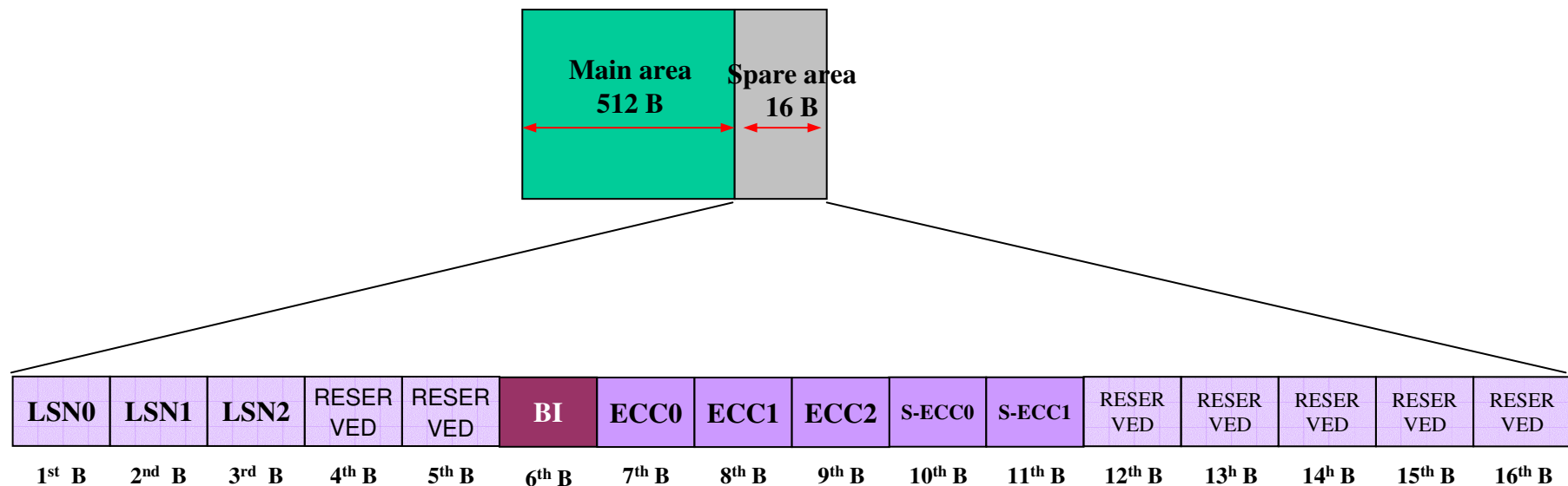
27. April. 2005

**Memory Division
Samsung Electronics Co., Ltd**

SPARE AREA ASSIGNMENT STANDARD

Category 1. Small Page(528B) & X8 org. NAND Flash

- Device : 64Mb, 128Mb, 256Mb, 512Mb, 1Gb DDP

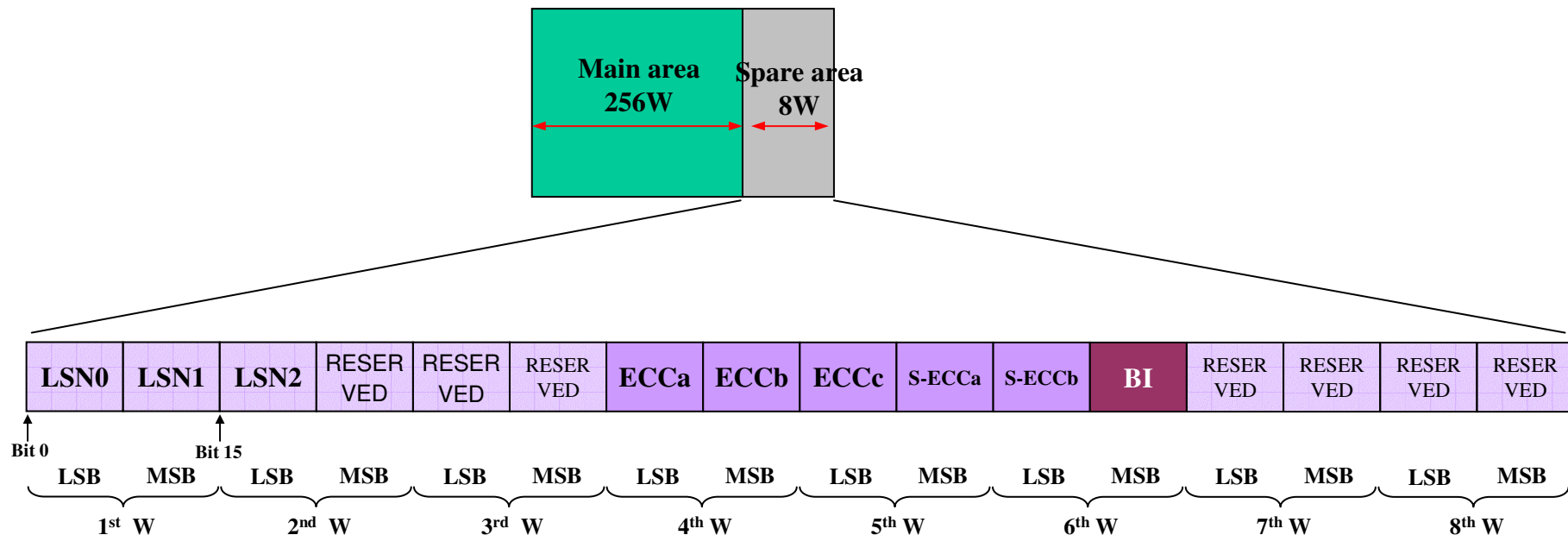


- > LSN : Logical Sector Number
- > ECC0,ECC1,ECC2 : ECC code for Main area data
- > S_ECC0,S_ECC1 : ECC code for LSN data
- > BI : Bad block Information

SPARE AREA ASSIGNMENT STANDARD

Category 2. *Small Page(264W) & X16 org. NAND Flash*

- Device : 64Mb, 128Mb, 256Mb, 512Mb, 1Gb DDP

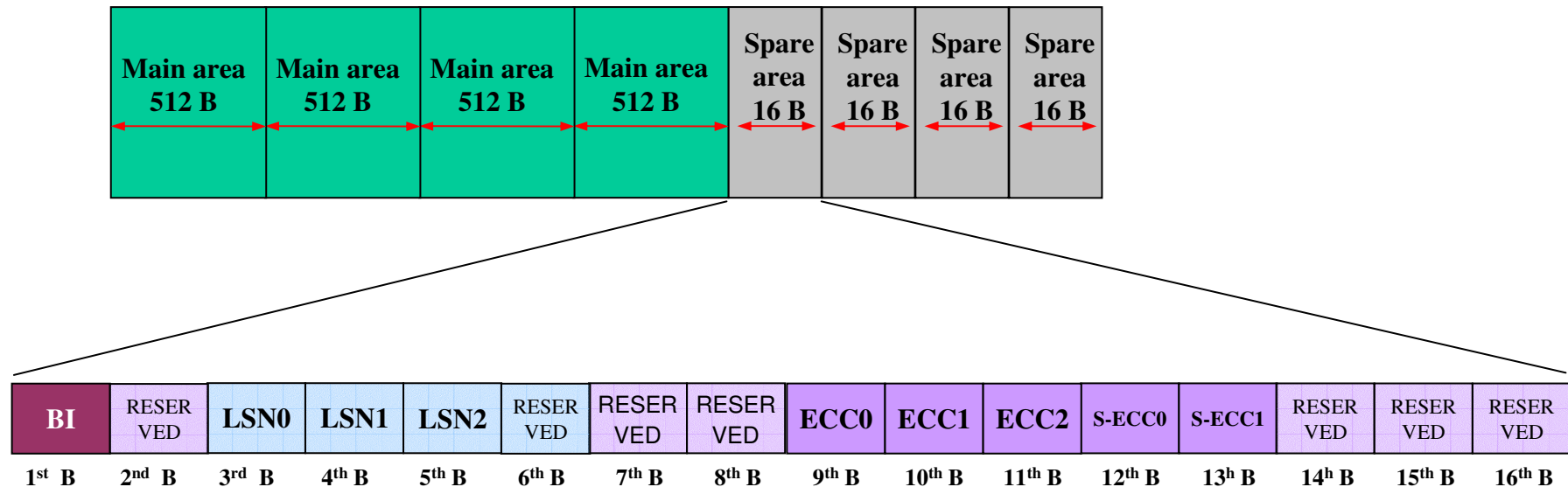


- > LSN : Logical Sector Number
- > ECCa,ECCb,ECCc : ECC code for Main area data
- > S_ECCa,S_ECCb : ECC code for LSN data
- > BI : Bad block Information

SPARE AREA ASSIGNMENT STANDARD

Category 3. Large Page(2KB) & X8 org. NAND Flash

- Device : 1Gb, 2Gb, 4Gb ~

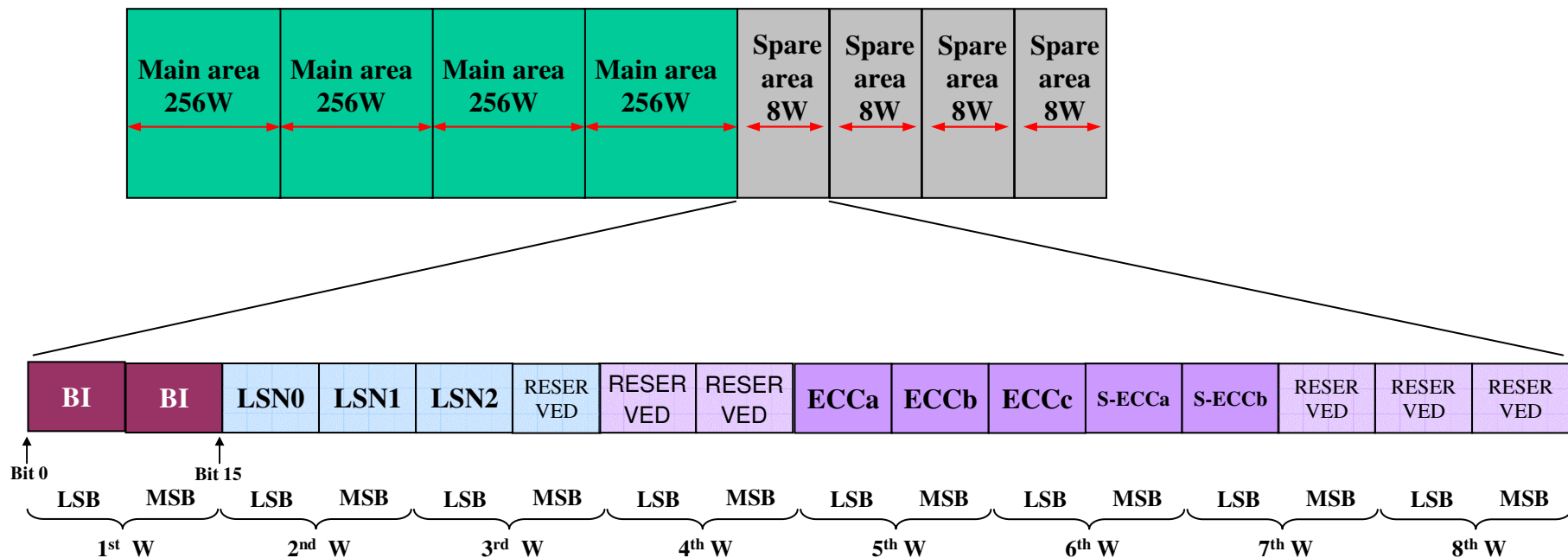


- > LSN : Logical Sector Number
- > ECC0,ECC1,ECC2 : ECC code for Main area data
- > S_ECC0,S_ECC1 : ECC code for LSN data
- > BI : Bad block Information

SPARE AREA ASSIGNMENT STANDARD

Category 4. Large Page(1KW) & X16 org. NAND Flash

- Device : 1Gb, 2Gb, 4Gb ~



- > LSN : Logical Sector Number
- > ECCa,ECCb,ECCc : ECC code for Main area data
- > S_ECCa,S_ECCb : ECC code for LSN data
- > BI : Bad block Information